

L Number	Hits	Search Text	DB	Time stamp
-	1	("6714939").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 13:37
-	1	("6714902").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 13:41
-	2	("6167359").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 13:47
-	1064	(synthesiz\$3 same circuit) and (optimiz\$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 13:54
-	270	((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz\$3 same circuit) and (optimiz\$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 17:56
-	93	(partition\$3 or divid\$3) and (core or shell) and (((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz\$3 same circuit) and (optimiz\$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 14:44
-	18	(partition\$3 or divid\$3) and core and shell and (((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz\$3 same circuit) and (optimiz\$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 14:03
-	143	((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same optimiz\$5) and (((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz\$3 same circuit) and (optimiz\$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 14:43
-	58	(partition\$3 or divid\$3) and (core or shell) and (((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same optimiz\$5) and (((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz\$3 same circuit) and (optimiz\$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 14:45
-	192	(floorplann\$3 or (floor adj1 plann\$3) or placement or placing or (physical adj1 design)) with synthesi\$4 with (before of after)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 18:04

-	13	((partition\$3 or divid\$3) and (core or shell) and (((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz\$3 same circuit) and (optimiz\$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell)))) and ((floorplann\$3 or (floor adj1 plann\$3) or placement or placing or (physical adj1 design)) with synthesi\$4 with (before of after))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 18:03
-	9	(resynthesi\$4 or re-synthesi\$4 or recombina\$3) and (((partition\$3 or divid\$3) and (core or shell) and (((floorplann\$3 or (floor adj1 plann\$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz\$3 same circuit) and (optimiz\$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell)))) and ((floorplann\$3 or (floor adj1 plann\$3) or placement or placing or (physical adj1 design)) with synthesi\$4 with (before of after)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 18:05
-	25424	(synthesi\$4 same (partition\$3 or divid\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:34
-	78	((synthesi\$4 same (partition\$3 or divid\$3)) ) and (optimiz\$5 near2 plac\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 08:35
-	20	(layer same length) and (((synthesi\$4 same (partition\$3 or divid\$3)) ) and (optimiz\$5 near2 plac\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 08:35
-	20	(buffer or repeater) and ((layer same length) and (((synthesi\$4 same (partition\$3 or divid\$3)) ) and (optimiz\$5 near2 plac\$5)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 08:36
-	11	((pin or point or port) same wir\$3) and ((buffer or repeater) and ((layer same length) and (((synthesi\$4 same (partition\$3 or divid\$3)) ) and (optimiz\$5 near2 plac\$5))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:32
-	1216	(deep or sub-micron or (sub adj1 micron)) near2 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:33
-	1132	(deep or sub-micron or (sub adj1 micron)) adj4 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:33
-	40	synthesi\$4 and (partition\$3 or divid\$3) and ((deep or sub-micron or (sub adj1 micron)) adj4 circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:35
-	16	((wir\$3 or net or line or conductor or lead) with layer) and (synthesi\$4 and (partition\$3 or divid\$3) and ((deep or sub-micron or (sub adj1 micron)) adj4 circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:39